

PRODUCT SPECIFICATION

20*4 Characters COB LCD MODULE MODEL: LT-2004C-602 Ver:2.0

< \diamond > Finally Specification

	CUSTOMER'S	S APPROVAL
CUSTOMER :		
SIG	NATURE:	DATE:

APPROVED	PM	PD	PREPARED
BY	REVIEWD	REVIEWD	Ву

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• This specification is subject to change without notice. Please contact LT or it's representative before designing your product based on this specification.

Revision Status

Version	Revise Date	Content	Modified By
VER 1.0	09/06/11	First Issued	
VER 2.0	10/02/04	Modify the backlight brightness based on FS-1.0	

	Issued Date: 2010.02.04 Doc. No.:
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1. Features

The features of LCD are showed as follows

- * Display mode : FSTN, Transflective/Positive
- * Controller IC : SPLC780D1-001(English-Japanese)
- * Display format : 20X4Characters
- * Interface Input Data : 8 Bit
- * Driving Method : 1/16Duty, 1/4Bias
- * Viewing Direction : 6 O'clock
- *Backlight : LED/White(side)
- *Sample NO. : EC2004B1FSW6B-2.0/20100203

2. MECHANICAL SPECIFICATIONS

ltem	Specification	Unit
Module Size	146(W) x62.5(H) x13.6MAX(D)	mm
Viewing Area	123.5(H) x 43(V)	mm
Activity Display Area	118.84(H)x38.47(V)	mm
Character Font	5x8 Dots	-
Character Size	4.84(H)x9.22(V)	mm
Character Pitch	6.0(H)x9.75(V)	mm
Dot Size	0.92(H)x1.1(V)	mm

3. ELECTRICAL SPECIFICATIONS

3-1 ABSOLUTE MAXIMUM RATINGS (Ta = 25 °C)

Item	Symbol	Min	Max	Unit
Supply Voltage For Logic	Vdd	0.3	7.0	V
Supply Voltage For LCD Drive	V _{LCD}	VDD-10	VDD+0.3	V
Input Voltage	Vin	-0.3	VDD+0.3	V
Operating Temp.	Тор	-20	+70	°C
Storage Temp.	Tst	-30	+80	°C

*. NOTE: The response time will be extremely slow when the operating temperature is around -10 $^{\circ}$ C, and the back ground will become darker at high temperature operating.

3-2 ELECTICAL CHARACTERISTICS

ltem		Symbol	Symbol Test Min. Condition		Тур.	Max.	Unit
Logic supply Voltage		Vdd – Vss	Ta = 25 °C	4.5	5	5.5	V
LCD Drive		V _{OP} =VDD-V0		4.2	4.5	4.8	V
Input Voltage	"H" Level	V _{IH}	$VDD=5V\pm5\%$	0.7VDD	-	Vdd	V
1	"L" Level	V _{IL}		-0.3	-	0.55	V
Frame Frequency		f _{FLM}	VDD = 5V	-	78.1		Hz
Current Cons	umption	I _{DD}	Vdd = 5V	-	1.6	-	mA

3-3 BACKLIGHT

3-3-1. Absolute Maximum Ratings

ltem	Symbol	Condition	Min.	Тур.	Max.	Unit
Forward Current	IF		-	-	150	mA
Reverse Voltage	VR	Ta = 25 °C	-	-	5	V
Power Dissipation	PD		-	-	480	mW

3-3-2. Electrical-optical Characteristics

ltem	Symbol	Condition	Mi	n.	Тур	•	Ма	ix.	Unit				
Forward Voltage	VF	lf=90mA	_		4.5		-		V				
i olivara voltago	•	Ta = 25 °C				0			v				
Reverse Current (per LED)	lr	Vr=5v	-	-		-		60		60			uA
Average Luminous Intensity	lv	Ta = 25 °C If=90mA	450		60	00			cd/m ²				
Color Coordinatoo	_	Ta = 25 °C	Χ	Y	Х	Y	Х	Y					
Color Coordinates		lf=90mA		0.26	-	-	0.31	0.31					

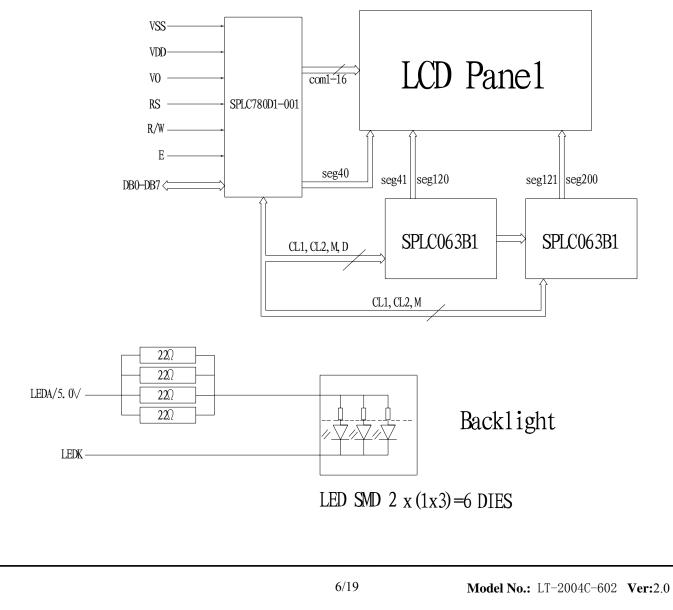
The brightness is measured without LCD panel

4. TERMINAL FUNCTIONS AND BLOCK DIAGRAM

4-1 INTERFACE PIN FUNCTION DESCRIPTION

PIN NO.	SYMBOL	FUNCIONS
1	VSS	Ground
2	VDD	Supply voltage for logical circuit
3	V0	Supply voltage for LCD driving
4	RS	Select register signal
5	R/W	Select read or write signal
6	Е	Enable signal
7-14	DB0~DB7	Data Bus
15	LEDA	Backlight(+5V)
16	LEDK	Backlight(-)
17	N/C	Not Connect
18	N/C	Not Connect

4-2 BLOCK DIAGRAM

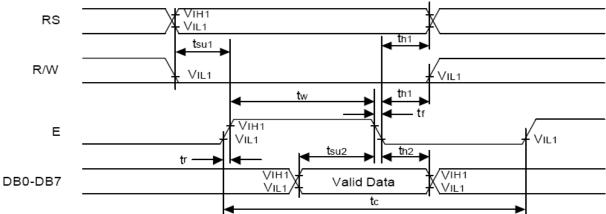


5. TIMING CHARACTERISTICS

5 - 1 Write mode

Mode	Characteristics	Symbol	Min	Тур	Max	Unit
Write Mode	E Cycle Time	tc	500	-	-	ns
(refer to Figure-6)	E Rise/Fall Time	t _R , t _F	-	-	20	
	E Pulse Width (High, Low)	tw	230	-	-	
	R/W and RS Setup Time		40	Ι	-	
	R/W and RS Hold Time	t _{H1}	10	-	-	
	Data Setup Time	tsu2	80	-	-	
	Data Hold Time	t _{H2}	10	_	-	

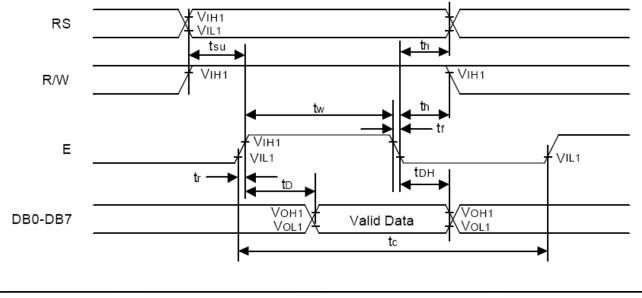
5-2 Write mode timing diagram



5.3 Read mode

Read Mode	E Cycle Time	tc	500	-	-	ns
(refer to Figure-7)	E Rise/Fall Time	t _R , t _F	-	-	20	
	E Pulse Width (High, Low)	tw	230	_	_	
	R/W and RS Setup Time	tsu	40	-	-	
	R/W and RS Hold Time	t _H	10	-	-	
	Data Output Delay Time	tD	_	_	120	
	Data Hold Time	tDH	5	_	-	

5-4Read mode timing diagram



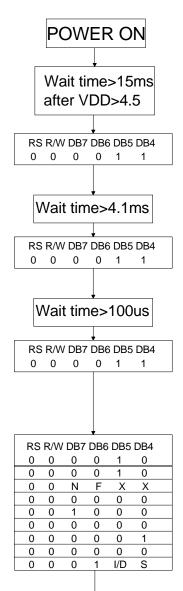
6. COMMAND LIST

Instruction		-	-	Ins	tructi	on Co	ode	_	_	-	Description	Execution
	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Instruction Code	time(fsoc=270)
Clear Display	0	0	0	0	0	0	0	0	0	1	Write "20H" to DDRAM. and set DDRAM address to "00H" from AC.	1.53ms
Return Home	0	0	0	0	0	0	0	0	1	х	Set DDRAM address to "00H" from AC and return cursor to its original position if shifted. The contents of DDRAM are not changed.	1.53ms
Entry Mode Set	0	0	0	0	0	0	0	1	I/D	SH	Assign cursor moving direction and make shift of entire display enable.	39µs
Display ON/OFF Control	0	0	0	0	0	0	1	D	С	В	Set display(D), cursor(C), and blinking of cursor(B) on/off control bit.	39µs
Cursor or Display Shift	0	0	0	0	0	1	S/C	R/L	x	х	Set cursor moving and display shift control bit, and the direction, without changing DDRAM data.	39µs
Function Set	0	0	0	0	1	DL	N	F	х	х	Set interface data length (DL : 4-bit/8-bit), numbers of display line (N : 1-line/2-line), display font type (F : 5 X 8 dots/ 5 X 11 dots)	39µs
Set CGRAM Address	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0	Set CGRAM address in address counter.	39µs
Set DDRAM Address	0	0	1	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Set DDRAM address in address counter.	39µs
Read Busy Flag and Address	0	1	BF	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.	0µs
Write Data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write data into internal RAM (DDRAM/CGRAM).	43µs
Read Data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read data from internal RAM (DDRAM/CGRAM).	43µs

NOTE: When an MPU program with checking the Busy Flag (DB7) is made, it must be necessary 1/2 fosc is necessary for executing the next instruction by the falling edge of the 'E' signal after the Busy Flag (DB7) goes to "LOW".

	Issued Date: 2010.02.04 Doc. No.:
6.2 8-bit Initialization seque	ence
POWER ON	
Wait time>15ms after VDD>4.5	Wait time > 40ms After VDD > 2.7V
RS R/W DB7 DB6 DB5 DB4 DB3 DB2 DB1 DB0 0 0 0 0 1 1 X X X X	BF cannot be checked before this instruction . Function set (Interface is 8 bits length .)
Wait time>4.1ms RS R/W DB7 DB6 DB5 DB4 DB3 DB2 DB1 DB0 0 0 0 0 1 1 X X X X Wait time>100us	BF cannot be checked before this instruction . Function set (Interface is 8 bits length .)
RS R/W DB7 DB6 DB5 DB4 DB3 DB2 DB1 DB0 0 0 0 0 1 1 X X X X	BF cannot be checked before this instruction . Function set (Interface is 8 bits length .)
	BF can be checked after the following instructions
RS R/W DB7 DB6 DB5 DB4 DB3 DB2 DB1 DB0 0 0 0 1 N F X	Function set (Interface is 8 bits length . Specify the number of display lines and character font .)
0 0 0 0 0 1 0 0 0 0 0 0 0 0 0 0 1 0 0 0 0 0 0 0 0 1	The number of display lines and character font cannot be changed afterwards .
Initialization Ends	Display off Display clear Entry mode set

6.3 4-bit Initialization sequence



Initialization Ends

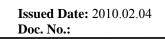
Wait time > 40ms After VDD > 2.7V

- BF cannot be checked before this instruction . Function set (Interface is 8 bits length .)
- BF cannot be checked before this instruction .
 Function set (Interface is 8 bits length .)
- BF cannot be checked before this instruction . Function set (Interface is 8 bits length .)
 - BF can be checked after the following instructions
 - Function set (Interface is 4 bits length . Specify the number of display lines and character font .)
 - The number of display lines and character font cannot be changed afterwards .
 - Display off Display clear Entry mode set

7. CHARACTER GENERATOR ROM

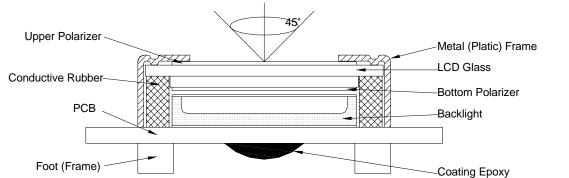
Upp # 4 Lewer Bits	00 00	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	11 00	1 101	1110	1111
XXXX0000	CG RAM (1)	0001	0010	Ø		P	-	F	1000	1001	1010	_	5		C:C	p
xxxx0001	(2)		•	1	A	Q	a	9			٠	7	Ŧ	4	ä	q
xxxx0010	(3)			2	B	R	Ь	r				1	Ņ	×	F	8
xxxx0011	(4)		#	3	C	5	C	S			┛	ゥ	Ŧ	Ŧ	3	60
xxxx0100	(5)		\$	4	D	T	d	ŧ.			ς.	I	ŀ	Þ		Ω
xxxx0101	(6)			5		U	e	IJ				7	*	1	G	ü
xxxx0110	(7)		8	6		Ų	f	V			Ţ	ħ	-		ρ	Σ
xxxx0111	(8)		7	7	G	ļ,]	9	W			Ņ	Ŧ	7	7	G	π
xxxx1000	(1)		ζ	8		Х	h	X			4	2	7	IJ	٦.	X
xxxx1001	(2))	9	I	Υ	1	ч			Ċ	ካ	J	IL	-1	Ч
xxxx1010	(3)		¥		J	Ζ	j	Z			I		i)	Ŀ	j	Ŧ
xxxx1011	(4)		╋	7	Κ		k	{			7	ţ	F		X	Ħ
xxxx1100	(5)		7	<		¥					Þ	Ð	7	7	4	Ħ
xxxx1101	(6)		-		Ņ]	Pi	}			ユ	Ζ,		-	Ł	÷
xxxx1110	(7)					~	ľ	÷			3	Ð	7	27	n	
xxxx1111	(8)		/	?	0		0	÷			ų	y	3	•	Ő	

Note: The user can specify any pattern for character-generator RAM.



8. QUALITY SPECIFICATIONS

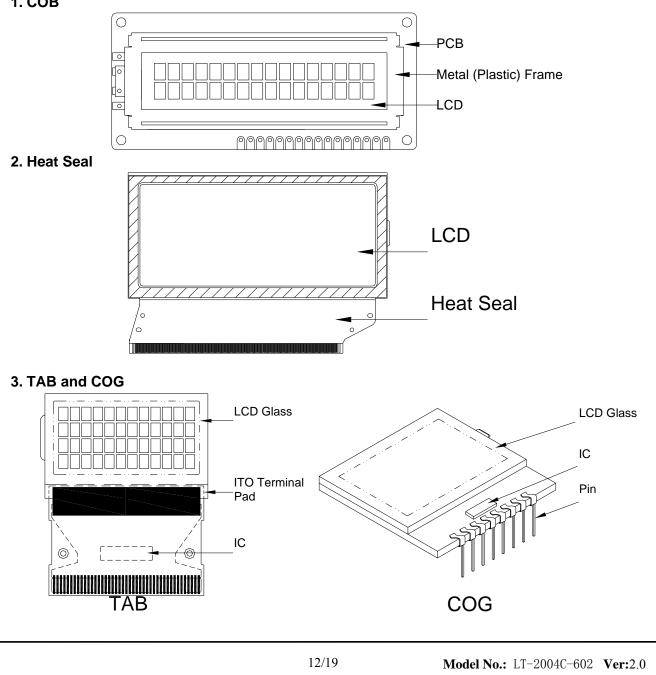
- 8 1. LCM Appearance and Electric inspection Condition
- 1. Inspection will be done by placing LCM 30cm away from inspector's eyeballs under normal illumination.



2. View Angle: with in 45° around perpendicular line.

8-2. Definition





8-3. Sampling Plan and Acceptance

1.Sampling Plan

MIL - STD - 105E (\parallel) ordinary single inspection is used.

2.Acceptance	
Major defect:	AQL = 0.25%
Minor defect:	AQL = 0.65%

8-4. Criteria

1.COB

Defect	Inspection Item	Inspection Standards	
Major	PCB copper flakes peeling off	Any copper flake in viewing Area should be greater than 1.0mm ²	Reject
Major	Height of coating epoxy	Exceed the dimension of drawing	Reject
Major	Void or hole of coating epoxy	Expose bonding wire or IC	Reject
Major	PCB cutting defect	Exceed the dimension of drawing	Reject

2. SMT

Defect	Inspection Item	Inspection Standa	ards
Minor	Component marking not readable		Reject
Minor	Component height	Exceed the dimension Of drawing	Reject
Major	Component solder defect (missing , extra, wrong component or wrong orientation		Reject
Minor	Component position shift x component soldering pad x \rightarrow	X < 3/4Z Y > 1/3D	Reject
Minor	Component tilt component D Soldering pad	Y > 1/3D	Reject
Minor	Insufficient solder component θ PAD PCB	<i>θ</i> ≤ 20°	Reject

3. Metal (Plastic) Frame

Defect	Inspection Item	l II	nspection Standa	rds	
Major	Crack / breakage	Any	/where	Reject	
		W	L	Acceptable of Scratch	
		w<0.1mm	Any	Ignore	
		0.1 <u><</u> w<0.2mm	L <u><</u> 5.0mm	2	
Minor	Frame Scratch	0.2 <u><</u> w<0.3mm	L <u><</u> 3.0mm	1	
		w <u>></u> 0.3mm	Any	0	
		Note: 1. Above criteria applicable to scratch line with distance greater than 5mm. 2. Scratch on the back side of frame (no visible) can be ignored.			
				Acceptable of Dents / Pricks	
		⊕ ≤	2		
	Frame Dent, Prick	1.0<	1		
Minor	$\Phi = \frac{L + W}{2}$	1.5r	0		
	2	Note : 1. Above criteria applicable to any two der / pricks with distance greater than 5mm 2. Dent / prick on the back side of frame (r visible) can be ignored			
Minor	Frame Deformation	Excee	d the dimension of	drawing	
Minor	Metal Frame Oxidation		Any rust		

4. Flexible Film Connector (FFC)

Defect	Insp	ection Item	Inspection Standa	rds
Minor	Tilted soldering		Within the angle +5°	Acceptable
Minor	Uneven solder joint /bump			Reject
			Expose the conductive line	Reject
Minor	Hole	$\Phi = \frac{L + W}{2}$	Φ > 1.0mm	Reject
Minor	Minor Position shift		Y > 1/3D	Reject
Minor		▲ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓	X > 1/2Z	Reject

5. Screw

Defect	Inspection Item	Inspection Standards	
Major	Screw missing/loosen		Reject
Minor	Screw oxidation	Any rust	Reject
Minor	Screw deformation	Difficult to accept screw driver	Reject

6. Heatseal 、 TCP 、 FPC

Defect	Inspection Item	Inspection Standards	
Major	Scratch expose conductive layer		Reject
Minor	HS Hole $\Phi = \frac{L + W}{2}$	Φ > 0.5mm	Reject
Major	Adhesion strength	Less than the specification	Reject
Minor	Position shift $Y \xrightarrow{-\frac{1}{2}} \xrightarrow{-\frac{1}{2}$	Y > 1/3D	Reject
Minor		X > 1/2Z	Reject
Major	Conductive line break		Reject

7. LED Backing Protective Film and Others

Defect	Inspection Item	Inspection Standards				
		Acceptable number of units				
		⊕ <u><</u> 0.10mm	ignore			
		0.10<⊕ <u><</u> 0.15mm	2			
Minor	LED dirty, prick	0.15<⊕ <u><</u> 0.2mm	1			
		Φ >0.2mm	0			
		The distance between any two spots should be \geq 5mm Any spot/dot/void outside of viewing area is acceptable				
Minor	Protective film tilt	Not fully cover LCD	Reject			
Major	COG coating	Not fully cover ITO circuit	Reject			

8. Electric Inspection

Defect	Inspection Item	Inspection Standards	
Major	Short		Reject
Major	Open		Reject

9. Inspection Specification of LCD Defect **Inspect** Item Inspection **Standards** 0.03<W<0.05 W W<0.03 W>0.05 * Glass Scratch L L<5 L<3 Any * Polarizer Scratch ACC. Minor Linear Defect 1 1 Reject * Fiber and Linear NO. material Note L is the length and W is the width of the defect 0.1<⊕<u><</u>0.15 0.15<⊕<u><</u>0.2 Φ<u><</u>0.1 Foreign material Φ Φ**>0.2** * between glass and ACC. 3EA / 2 1 0 <u>100</u>mm² Black Spot and polarizer or glass NO. Minor Polarizer and glass Pricked * Polarizer hole or Φ is the average diameter of the defect. Note Distance between two defects > 10mm. protuberance by external force Unobvious * Ф<u><</u>0.3 0.3<⊕<u><</u>0.5 Φ **0.5<**Φ transparent foreign ACC. 3EA / 100mm² 0 1 material between NO. White Spot glass and glass or and Bubble in Minor glass and polarizer polarizer Φ is the average diameter of the defect. * Air protuberance Note Distance between two defects > 10mm. between polarizer and glass 0.10<⊕<u><</u>0.20 Φ>0.25 Φ **⊕**<0.10 0.20<⊕<u><</u>0.25 ACC. 3EA / 0 2 1 100mm² NO. Segment W is more than 1/2 segment width Reject Minor Defect Φ= <u>L+W</u> Note 2 Distance between two defect is 10mm Φ**>0.25** Φ Φ**<0.10** 0.10<0<0.20 0.20<0<25 W<1/2 Seg W<1/2 Seg W Glue Ignore W<0.2 W<0.2 Protuberant Minor Segment ACC. 3EA / 2 1 0 NO. 100mm² $\Phi = (L + W) / 2$ 1. Segment В B<0.4mm 0.4<B<1.0mm B>1.0mm B-A B-A<1/2B B-A<0.2 B-A<0.25 Assembly Minor Judge Acceptable Acceptable Acceptable Mis-alignment 2. Dot Matrix Deformation>2° Reject Accept when stains can be wiped lightly with a soft cloth Stain on LCD Minor or a similar one. Otherwise, judged according to the Panel Surface above items: "Black spot" and "White Spot"

9. RELIABILITY

NO.	ltem	Condition	Criterion
1	High Temperature Operating	70℃, 96Hrs	No defect in cosmetic and operational functi on allowable. Total current Consumption should be below doub le of initial value.
2	Low Temperature Operating	-20℃, 96Hrs	
3	High Humidity	60℃, 90%RH, 96Hrs	
4	High Temperature Storage	80°C, 96Hrs	
5	Low Temperature Storage	-30℃, 96Hrs	
6	Vibration	Random wave	
		10 ~ 100Hz	
		Acceleration: 2g	
		2 Hrs per direction(X,Y,Z)	
7	Thermal Shock	-20℃ to 25℃ to 70℃	
		(60Min) (5Min) (60Min)	
		16Cycles	
8	ESD Testing	Contract Discharge Voltage: +1 ~ 5kV and -1 ~ -5kV	There will be discharged ten times at every discharging voltage cycle. The voltage gap is 1kV.
		Air Discharge Voltage: +1 ~ 8kV and -1 ~ -8kV	

Note: 1) Above conditions are suitable for our company standard products.

2) For restrict products, the test conditions listed as above must be revised.

10. HANDLING PRECAUTION

(1) Mounting Method

The panel of the LCD Module consists of two thin glass plates with polarizers, which easily get damaged since the Module is fixed by utilizing fitting holes in the printed circuit board. Extreme care should be taken when handling the LCD Modules.

- (2) Caution of LCD handling & cleaning
 - When cleaning the display surface, use soft cloth with solvent (recommended below) and wipe lightly.
 - Isopropyl alcohol
 - Ethyl alcohol
 - Tricolor trifler thane

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface. Do not use the following solvent:

- Water
- Kenton
- Aromatics
- (3) Caution against static charge
 - The LCD Module use C-MOS LSI drivers, so we recommend that you connect any unused input terminal to VDD or VSS, do not input any signals before power is turned on. And ground your body, Work/assembly table. And assembly equipment to protect against static electricity.

(4) Packaging

- Modules use LCD elements, and must be treated as such. Avoid intense shock and falls from a height.
- To prevent modules from degradation. Do not operate or store them exposed directly to sunshine or high temperature/humidity.
- (5) Caution for operation
 - It is indispensable to drive LCD's within the specified voltage limit since the higher voltage than the limit shorten LCD life. An electrochemical reaction due to direct current causes LCD deterioration, Avoid the use of direct current drive.
 - Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD's show dark color in them. However those phenomena do not mean malfunction or out of order with LCD's .which will come back in the specified operating temperature range.
 - If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
 - -A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.
 - Usage under the relative condition of 40°C, 50%RH or less is required.
- (6) Storage
 - In the case of storing for a long period of time, (For years) for the purpose or replacement use, the following ways are recommended.
 - Storage in a polyethylene bag with sealed so as not to enter fresh air outside in it, And with no desiccant.
 - Placing in a dark place where neither exposure to direct sunlight nor light is. Keeping temperature in the specified storage temperature range.
 - Storing with no touch on polarizer surface by the anything else. (It is recommended to store them as they have been contained in the inner container at the time of delivery)
- (7) Safety
 - It is recommendable to crash damaged or unnecessary LCD into pieces and wash off liquid crystal by using solvents such as acetone and ethanol. Which should be burned up later.

When any liquid crystal leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water.

